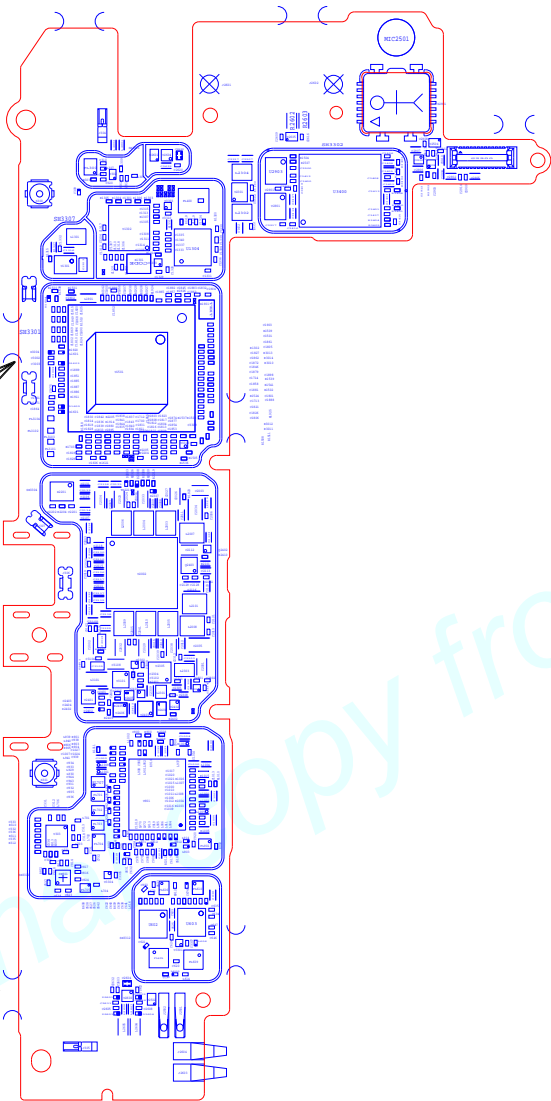



REVISIONS				
REV	ECN	DESCRIPTION	DFTS	DATE
1	40348	PRODUCTION RELEASE	A. LEVO	2012/11/22
2	41002	PRODUCTION RELEASE	A. LEVO	2013/01/07

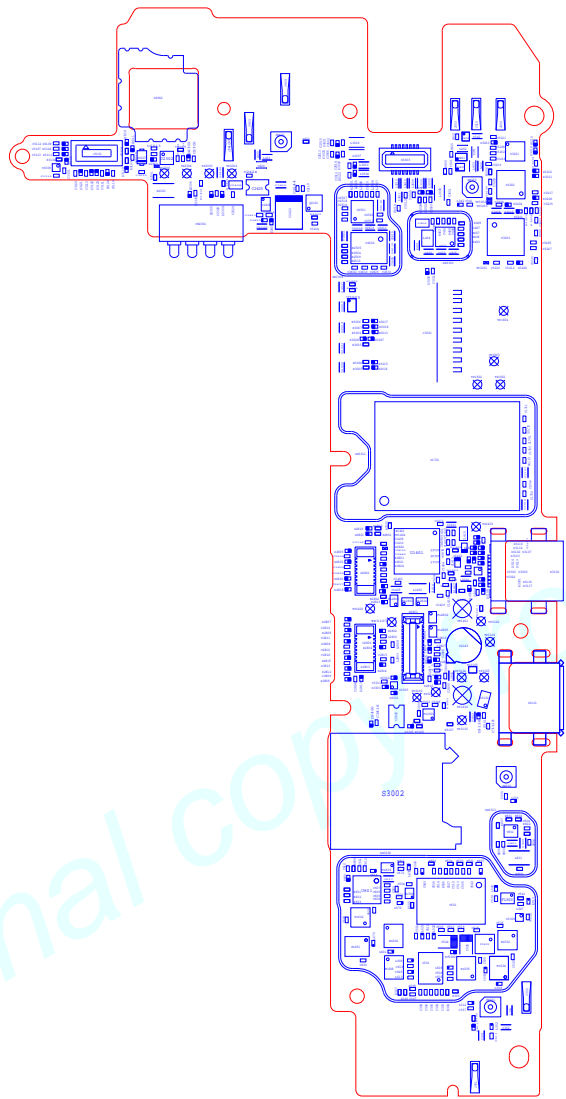
THE SOLDER AND ASSEMBLY PROCESS SHALL COMPLY WITH
DIRECTIVE 2002/95/EC (ROHS) AND RIM ENVIRONMENTAL
SPECIFICATIONS

VESTIGE SHOULD NOT BE
PROUD OF ADJACENT BOARD
EDGE AFTER ROUTING FROM
PANEL ARRAY. (ALL PLACES)




PCB ASSEMBLY VIEWED FROM PRIMARY SIDE (SIDE A)

 RESEARCH IN MOTION COPYRIGHT 2013			
TITLE		PRINTED WIRING BOARD R070 Main PCB	
RIM CONFIDENTIAL		REV: 2	
APPROVALS		DATE	A
DRAWN BY: A. LEVO		13.01.07	SIZE
			DWG. NO. POP-44900-010
			SCALE 1:1
			SHEET 1 of 2



PCB ASSEMBLY VIEWED FROM SECONDARY SIDE (SIDE B)

<div><div></div><div>RESEARCH IN MOTION COPYRIGHT</div></div>				
TITLE		PRINTED WIRING BOARD R070 Main PCB		
RIM CONFIDENTIAL		REV: 2		
APPROVALS		DATE	A	DWG. NO. POP-44900-010
DRAWN BY: A. LEVO		13.01.07	SIZE	SCALE 1:1
				SHEET 2 of 2